

PTO: 2005-4381

Japanese Published Unexamined Patent Application (A) No. 62-141824, published June 25, 1987; Application Filing No. 60-281220, filed December 16, 1985; Inventor(s): Tatsuo Watanabe; Assignee: Kokusai Denshin Denwa Corporation (International Telegraph and Telephone Corporation); Japanese Title: Non-Linear Compensation Systems

NON-LIENAR COMPENSATION SYSTEMS

CLAIM(S)

A non-linear distortion compensation system, which has a reverse distortion generation circuit for generating the reverse distortion output that indicates the distortion characteristic reverse to the distortion characteristic of an amplifier to be compensated, by charging an input signal into the non-linear element, and which extracts the composite output of a delay output of said input signal and of said reverse distortion output to compensate the distortion in the output from said amplifier to be compensated; said system being characterized in that said reverse distortion generation circuit is constructed by arranging in parallel multiple distortion output circuits each having a different operation level.